

# GaAs SPST Absorptive Switch with ASIC Driver, DC-3.0 GHz

M/A-COM Products Rev. 4

#### **Features**

- Typical Isolation: 42 dB (2,000 MHz)
- Typical Insertion Loss: 1.8 dB (2,000 MHz)
- Integral ASIC TTL/CMOS Driver
- Low DC Power Consumption
- 50 Ohm Nominal Impedance
- Tape and Reel Packaging Available
- Test Boards Available
- Lead-Free SOW-24 Package
- 100% Matte Tin Plating over Copper
- Halogen-Free "Green" Mold Compound
- 260°C Reflow Compatible
- RoHS\* Compliant Version of SW65-0014

### **Description**

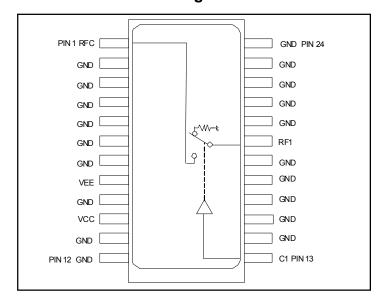
M/A-COM's MASW-007076-000100 is a GaAs MMIC absorptive SPST switch with an integral silicon ASIC driver. This device is in a 24-lead plastic package. This switch offers excellent broadband performance and repeatability from DC to 3 GHz, while maintaining low DC power dissipation. The MASW-007076-000100 is ideally suited for wireless infrastructure applications. Also available in ceramic package with improved performance.

### **Ordering Information**

Part Number	Package
MASW-007076-000100	Bulk Packaging
MASW-007076-0001TR	1000 piece reel
MASW-007076-0001TB	Sample Test Board

Note: Reference Application Note M513 for reel size information.

### **Functional Block Diagram**



### **Pin Configuration**

Pin No.	Function	Pin No.	Function
1	RFc	13	C1
2	GND	14	GND
3	GND	15	GND
4	GND	16	GND
5	GND	17	GND
6	GND	18	GND
7	GND	19	RF1
8	V <sub>EE</sub>	20	GND
9	GND	21	GND
10	V <sub>cc</sub>	22	GND
11	GND	23	GND
12	GND	24	GND

PRELIMINARY: Data Sheets contain information regarding a product M/A-COM has under development. Performance is based on engineering tests. Specifications are typical. Mechanical outline has been fixed. Engineering samples and/or test data may be available. Commitment to produce in volume is not guaranteed.

<sup>\*</sup> Restrictions on Hazardous Substances, European Union Directive 2002/95/EC.

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Electrical Specifications:  $T_A = 25^{\circ}C$ ,  $Z_0 = 50\Omega$ 

Parameter	Test Conditions	Units	Min	Тур	Max
Insertion Loss	DC - 3.0 GHz	dB	_	1.8	2.2
Isolation (All arms off)	DC - 3.0 GHz	dB	35	42	_
VSWR	DC - 3.0 GHz On Off	_	_	1.6:1 2.0:1	2.1:1 2.1:1
T <sub>rise</sub> T <sub>fall</sub> T <sub>on</sub> T <sub>off</sub> Transients	10%/90%, 90%/10% <sup>1</sup> 50% TTL to 90%/10% RF In-band (peak to peak)	ns ns mV		15 50 50	50 150 150
1 dB Compression	.05 GHz .5 - 3.0 GHz	dBm dBm		+20 +27	_
Input IP3	Two tone inputs 0.05 GHz Up to +5 dBm 0.5 - 3.0 GHz	dBm dBm		+35 +46	_
Vcc	_	V	+4.5	+5.0	+5.5
VEE	_	V	-8.0	-5.0	-4.75
V <sub>IL</sub> V <sub>IH</sub>	LOW-level input voltage HIGH-level input voltage	V	0.0 2.0	_	0.8 5.0
lin (Input Leakage Current)	Vin = V <sub>CC</sub> or GND	uA	-1.0	_	1.0
Icc (Quiescent Supply Current)	Vcntrl = V <sub>CC</sub> or GND	uA	_	250	400
Δlcc (Additional Supply Current Per TTL Input Pin)	$V_{CC}$ = Max, Vcntrl = $V_{CC}$ - 2.1 V	mA	_	_	1.0
lee	VEE min to max, Vin = $V_{IL}$ or $V_{IH}$	mA	-1.0	-0.2	_

<sup>1.</sup> Decoupling capacitors (.01 μF) are required on the power supply lines.

### Absolute Maximum Ratings <sup>2,3</sup>

Parameter	Absolute Maximum
Max. Input Power 0.05 GHz 0.5 - 3.0 GHz <sup>4</sup>	+27 dBm +34 dBm
V <sub>CC</sub>	$-0.5V \le V_{CC} \le +7.0V$
V <sub>EE</sub>	$-8.5V \le V_{EE} \le +0.5V$
V <sub>CC</sub> - V <sub>EE</sub>	$-0.5V \le V_{CC} - V_{EE} \le 14.5V$
Vin <sup>5</sup>	$-0.5V \le Vin \le V_{CC} + 0.5V$
Operating Temperature	-40°C to +85°C
Storage Temperature	-65°C to +125°C

- 2. Exceeding any one or combination of these limits may cause permanent damage to this device.
- 3. M/A-COM does not recommend sustained operation near these survivability limits.
- 4. When the RF input is applied to the terminated port, the absolute maximum power is +30 dBm.
- 5. Standard CMOS TTL interface, latch-up will occur if logic signal is applied prior to power supply.

### **Truth Table**

devices.

damage:

**Handling Procedures** 

**Static Sensitivity** 

TTL Control Input	RF Common To:
C1	RF1
1	On
0	Off

Please observe the following precautions to avoid

Gallium Arsenide Integrated Circuits are sensitive to electrostatic discharge (ESD) and can be damaged by static electricity. Proper ESD control techniques should be used when handling these

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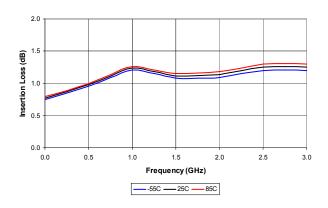


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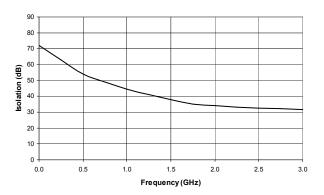
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### **Typical Performance Curves**

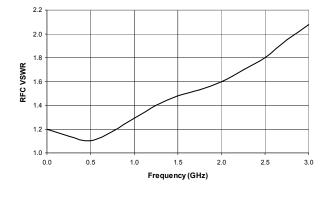
### Insertion Loss vs. Frequency



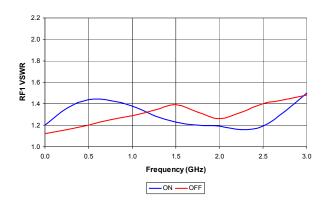
### Isolation Loss vs. Frequency



#### RFC VSWR vs. Frequency



#### RF1 VSWR vs. Frequency



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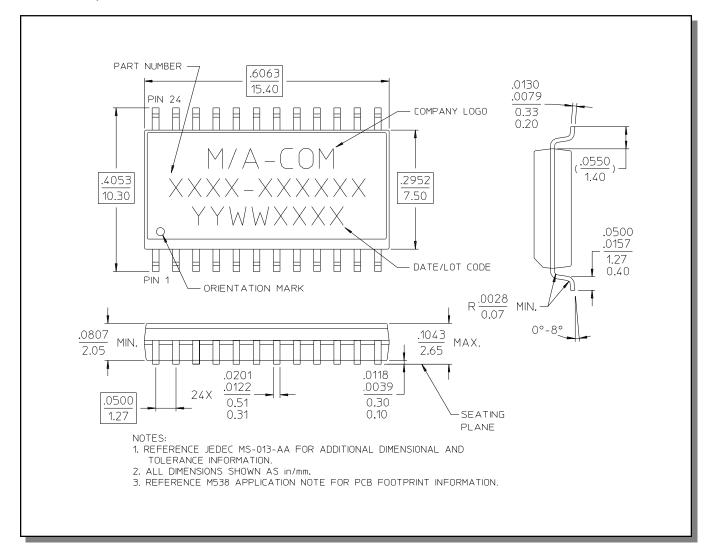
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### Lead-Free, SOW-24<sup>†</sup>



<sup>&</sup>lt;sup>†</sup> Reference Application Note M538 for lead-free solder reflow recommendations.

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